

Mr. BINOY D M PANICKER

Assistant Professor



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[Scholar Link](#)

Qualifications:

- BTech - Computer Science and Engg. (Mangalore University, Mangalore)
- M. Tech – Information Security (NIT Calicut)

Professional Experience:

- Assistant Professor, LBSITW, Trivandrum (2021-Till date)
- Assistant Professor, LBSCE, Kasaragod (2010-2021)
- Lecturer, LBSCE, Kasargodey (2002-2009)

Recently Taught Courses:

- Microprocessor and Microcontrollers
- Switching theory and logic design
- Networking and OS Lab
- Algorithm analysis and design
- Web Technology
- Web Technology lab

Other Responsibilities:

- Campus wide network in-charge (July 2014 – July 2018)
- HOD CSE (Jul 2015-Jun 2018)

- Staff Advisor (2021- Till date CSE-1)
- Staff Advisor (2015-2018 MCA)

Important Publications:

- **“Decentralised hierarchical approach based smart parking system “**, Binoy DM Panicker , Jizzy Ann George. Priyanka Surendran, IEEE Explore 30-Jun 2020,Publisher IET.
- **“Map Reduce framework for network motif finding“** , Binoy DM Panicker , Priyanka Surendran IEEE 2019 International Conference on Intelligent Computing, Instrumentation and Control Technologies (ICICT), 2019.