

Prof. Jaganath L C
Asst. Professor



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Qualifications:

- B. Tech – Electronics and Communication Engg. (Kerala University)
- ME – Communication Systems (Anna University)

Professional Experience:

- Assistant Professor, LBSITW, Trivandrum (2023-Till date)
- Assistant Professor, College of Engineering Aranmula (2017-2019)
- Associate Professor & HOD, PAACET, Trivandrum (2012-2016)
- Assistant Professor & HOD, PAACET, Trivandrum (2005-2012)
- Lecturer, Narayanaguru Engineering College, Tamilnadu (2004-2005)
- Lecturer, National College, Trivandrum (1999-2002)

Recently Taught Courses:

- Computer Architecture and Microcontroller
- VLSI Design
- Computer Vision
- Digital Electronics
- Robotics
- Network security
- Industrial safety Engineering

Other Responsibilities:

- Member in Series exam cell.

Important Publications:

- Microcontroller based system design

Membership in Professional Organizations

- ISTE Member
- IEEE Member

Conference/Paper Presentation:

- VLSI Design
- Field Programmable Gate Array
- Remote Sensing
- Image Processing
- Applying Robust Probabilistic Neural Network to Fault Tolerance for Security.